

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims

Claim 1 (Currently amended) A receiver for a CDMA system, intended to receive signals corresponding to spread spectrum information symbol streams ~~in~~ by pseudorandom binary sequences, said receiver comprising processing channels wherein:

at least one of said channels includes

a digital filter with coefficients adapted to one of said pseudorandom binary sequences having been used for information symbols spectrum spreading, and

a recovery circuit configured to produce a clock signal,

other channels each includes a sliding correlator working with another one of said pseudorandom binary sequences having been used for information symbols spectrum spreading, each sliding correlator being controlled by said clock signal produced by said recovery circuit of said at least one channel which includes said digital adapted filter.

Claim 2 (Cancelled).

Claim 3 (Currently Amended) The receiver according to Claim 1, wherein said digital adapted filter is configured to process said signals corresponding to spread spectrum information symbol streams.

Claim 4 (Currently Amended) The receiver according to Claim 1, wherein said digital adapted filter is configured to maintain said clock signal controlling said sliding correlators.

Claim 5 (Currently Amended) The receiver according to Claim 4, wherein said digital adapted filter is configured to maintain said clock signal permanently.

Claim 6 (Currently Amended) The receiver according to Claim 1, wherein ~~each~~the at least one of said channels further comprises a processing circuit connected to an output of said digital filter and the other channels each further comprises a processing circuit connected to an output of the corresponding sliding correlator.

Claim 7 (Currently Amended) The receiver according to Claim 6, wherein said processing ~~circuit is~~circuits are configured to perform a delayed multiplication.

Claim 8 (Currently Amended) The receiver according to Claim 6, wherein said processing ~~circuit is~~circuits are configured to perform a channel estimation.

Claim 9 (Currently Amended) The receiver according to Claim 1, wherein ~~each~~the at least one of said channels further comprises a decision circuit connected to an output of said digital filter and the other channels each further comprises a decision circuit connected to an output of the corresponding sliding correlator.

Claim 10 (Currently Amended) The receiver according to Claim 1, comprising a plurality of the processing channels, each including a filter adapted to one of said pseudorandom binary sequences.

Claim 11 (Currently Amended) The receiver according to Claim 10, wherein said plurality of processing channel produces a plurality of clock signals offset relative to each other.

Claim 12 (Currently Amended) A receiver for receiving signals comprising:
a first channel configured to process a first signal of said signals and to recover a clock signal from said first signal, a spectrum of said signals being spread using sequences, said first channel including a digital filter having coefficients adapted to a first sequence of said sequences; and

a second channel configured to process a second signal of said signals, said second channel being controlled by said clock signal recovered ~~by~~ from said first channel, said second channel being free of a clock signal recovering circuit.

Claim 13 (Previously Presented) The receiver according to Claim 12, wherein said first channel maintains said clock signal.

Claim 14 (Previously Presented) The receiver according to Claim 13, wherein said first channel maintains said clock signal permanently.

Claim 15 (Cancelled).

Claim 16 (Currently Amended) The receiver according to Claim ~~15~~ 12, wherein said sequences are at least one of pseudorandom or binary.

Claims 17-18 (Cancelled).

Claim 19 (Currently Amended) The receiver according to Claim 12, wherein said second channel includes a sliding correlator, said sliding correlator being configured to receive said second signal and being controlled by said clock signal.

Claim 20 (Previously Presented) The receiver according to Claim 12, comprising a third channel configured to process a third signal of said signals and to recover a second clock signal from said third signal.

Claim 21 (Previously Presented) The receiver according to Claim 20, wherein said clock signal recovered from said first signal and said second clock signal are offset relative to each other.

Claim 22 (Previously Presented) The receiver according to Claim 12, wherein said signals correspond to CDMA signals.